A/9 - Ajanta, Abhinav-Milind Residence: 022 2523 30 29
CHS, Shiv Srushty, Kurla
(EAST), Mumbai – 400 024. Mobile : 0983325 57 44

Harshawardhan P. Ahire

Objective	To present my achievements and attributes in front of you to understand my capabilities and scope of my work to justify appointment.
Experience	Aug 2008 – till date K.J. Somaiya Inst. Of Engineering & IT Sion, Mumbai **Asst. prof. in Electronics & Telecommunication Department** ▪ **Time table committee in charge** for the current semester. ▪ **Examination in charge** for the departmental exams. ▪ **Helping hand for lab setup** and **university documentation**.
	Aug 2008 – july 2010 K.J. Somaiya Inst. Of Engineering & IT Sion, Mumbai **Cultural In charge** ▪ Working for the **student welfare** activities in college. ▪ Formed **Surge club** for student's participation inter college festivals. ▪ Working as a **student council** in charge.
	Aug 2005 – July 2008 Rizvi College Of Engineering Bandra, Mumbai **Lecturer in Electronics Department** ▪ **Time table committee in charge**. ▪ **Incharge** for **table tennis and ring football** ▪ **Helping hand for lab setup** and **university documentation**.
	Jan 2006 – July 2008 Rizvi College Of Engineering Bandra, Mumbai **Sports in charge** ▪ Successfully arranged annual sports' for students in college single handedly ▪ Formed football team and cricket team of college to represent in inter college sports
	Jan 2006 – Jan 2007 Rizvi College Of Engineering Bandra, Mumbai **Training And Placement Officer** ▪ Successfully arranged more than campus interviews within the span of four months & placed more than 80 students. ▪ 60 % students from Civil Department are placed. ▪ Planning for 80-90% placement for next year.
	June 2003 –July 2005 Babasaheb Gawde Inst. Of Tech. Mumbai Central **Lecturer in Digital Electronics Department** ▪ Worked in **accreditation committee** to prepare necessary documents. ▪ Worked in **exam committee** as **billing supervisor**. ▪ Worked as **exhibition hall committee incharge** for **Thinkquest project exhibition.** ▪ Member of **academic monitoring committee**.

	June 2003 –July 2005 Babasaheb Gawde Inst. Of Tech. Mumbai Central **Industrial Liaison Officer** - Successfully organized **industrial tours** for students of third year and second year. - Arranged **campus interviews for wipro** - Attended seminar by **Microsoft as industrial liaison officer**.
	May 2002 –May 2003 Nageshwar Infotech Pvt. Ltd. Mumbai **Quality control & Hardware Engineer** - **Testing of new peripherals** coming in the shipment. - **Maintaining quality** of peripherals sold. - Solving **customer complaints** and **queries**. - **Assembling, Installing and Troubleshooting computer systems**.
	June 1995 – May 1997 Atlantic Computers Mumbai **Quality control & Hardware Engineer** - **Testing of new peripherals** purchased from the market. - Solving **customer complaints** and **queries**. - **Assembling, Installing and Troubleshooting computer systems.**
Education	**2017- till date Veermata Jijabai Technological Institute Matunga** - **Ph. D (Technology) in Electronics Engineering.** - Possessing satisfactory educational record. - Working on Error Correcting Codes**.**
	2010-2014 Fr. Concicao Rodrigus COE Bandra, Mumbai - **Masters of Engineering in Electronics Engineering.** - Possessing satisfactory educational record. - Project in **Brain Hemorrhage Detection using ANN.** - Secured **FIRST CLASS (61.00%).**
	1998–2002 MGM's College Of Engineering Nanded - **Bachelors of Engineering in Electronics and Telecommunication.** - Possessing satisfactory educational record. - Project in **EIGHT LINE TELEPHONE EXCHANGE.** - Secured **FIRST CLASS (64.00%).**
	1997–1998 Bombay Institute Of Technology Worli, Mumbai - **Diploma in Digital Electronics.** - Possessing satisfactory educational record. - Project in **PRINTER SHARING UNIT.** - Secured **FIRST CLASS (60.00%).**
	1996–1997 Government Polytechnic Mumbai Bandra, Mumbai - **Advance Diploma in Computer Software & System Analysis and Applications (ADCSSAA)** - Possessing satisfactory educational record. - Project in **SHARES ACCOUNTING**

	1992–1995 Vivekananda Edu. Society's Polytechnic Mumbai ▪ **Diploma in COMPUTER TECHNOLOGY**. ▪ Possessing satisfactory educational record. ▪ Project in **CARD OPERATED SYSTEM.** ▪ Secured **SECOND CLASS (59.08%).**
Publications	**"Brain Hemorrhage Detection using Artificial Neural Network",** **International Conference on Advances in Communication and Computing** **Technologies" August 2013.**
Projects	**BRAIN HEMORRAGE DETECTION USING ANN** ▪ Detection of ICH & SDH Hemorrhages ▪ Used Various Neural Networks for Detection. ▪ Used various number of Hidden Layer Neurons ▪ Its Matlab Based Simulation for detection. **ROLL:** ▪ Software Development. ▪ Algorithm Analysis and implementation. ▪ Testing & Troubleshooting.
	EIGHT LINE TELEPHONE EXCHANGE ▪ This is a private exchange. ▪ Permits to connect EIGHT Telephone Lines i.e. Eight Telephone Sets. ▪ It is completely hardware – based project which provides semi automatic operation. **ROLL:** ▪ Hardware Development and Implementation. ▪ Logic Development. ▪ Testing & Troubleshooting.
	PRINTER SHARING UNIT ▪ This allows three computer systems to share one printer. ▪ The sharing is done on the basis of time sharing. ▪ It is completely hardware – based project which serves reduction in system configuration. **ROLL:** ▪ Logic Development. ▪ Hardware Development and Implementation. ▪ Testing & Troubleshooting.
	SHARE ACCOUNTING AND TRANSFER ▪ It is software developed using FoxPro to maintain daily transactions as well as transfers of share. ▪ It performs transfer of shares, sell & purchase of shares, issuing of certificates. ▪ It is completely software – based project which provides every operation in share with the facilities of report and database maintenance.

	ROLL: ▪ Logic Development and Implementation. ▪ Debugging & error correction. ▪ Surveying market for gaining knowledge for the implementation.
	CARD OPERATED SYSTEM ▪ This is a Security system which enables only if appropriate card is used. ▪ If the card is correct than the user gets access if it is wrong than access is denied and alarm starts ringing. ▪ It is combination of hardware & software which provides semi automatic operation. **ROLL:** ▪ Programming on MICROPROCESSOR KIT using Assembly Level Language ▪ Hardware Development and Implementation. ▪ Debugging of program, Testing & Troubleshooting.
Extra curricular activities	▪ **Member of VISIOTECH – 2000 (National Level Paper Presentation) organizing committee.** ▪ Organized the **project exhibition** in **VISIOTECH – 2000.** ▪ Conducted **System Design Contest** in **VISIOTECH – 2000.**
Hobbies	▪ **Swimming, playing and watching indoor as well outdoor game, reading books.**
Interests & Activities	▪ **Travelling, long drives & computers.**
Personal Ethics	▪ **Good grasping ability.** ▪ **Like to work.** ▪ **Helping & understanding nature.** ▪ **Go getter attitude.** ▪ **Always a winner.** ▪ **Good in leadership qualities and representation.**
Subjects taught	**Diploma First year** ▪ EEBE: - Elements of Electronics & basic Electronics. ▪ EDC : - Electronic Devices & Components. **Diploma Second year** ▪ DTA: - Digital Techniques & Applications. ▪ LIC : - Linear Integrated Circuits. **Degree Sem. VII Final Year** ▪ Radar Engineering. ▪ Microwave Devices & Components **Degree Sem. V** ▪ Computer Organization & Architecture. ▪ Elements Of Microprocessor ▪ Principal of Control Engineering ▪ Random Signal Analysis

	Degree Sem. VIII Final Year ▪ Data Communication & Networking. ▪ Optical Fiber Communication ▪ Satellite Communications
Date Of Birth	**24th January 1977.**
References	▪ **Prof. Dr. D. P. Rathod (Ph. D. (Technology) in Electronics Engineering)** **Asst. Prof. in Department of Electronics Engineering** **V. J. T. I. Matunga .**